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DATE: August 17, 2006

TO: Examiner Duc T. Doan
U. S. Patent and Trademark Office

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FROM: Richard D. Ratchford, Jr.
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NO. OF PAGES TO FOLLOW: 2

MESSAGE:

Re: Applicant: Lee et al.
Serial No.: 10/762,170
Filed: January 21, 2004
**For: COMPUTER SYSTEM EMBEDDING SEQUENTIAL
BUFFERS THEREIN FOR IMPROVING THE
PERFORMANCE OF A DIGITAL SIGNAL PROCESSING
DATA ACCESS OPERATION AND A METHOD THEREOF**
Group Art Unit: 2188
Examiner: Doan, Duc T.
Docket: 8836-228 (IB12295US)

Attached please find the proposed Amendment to be entered by
the Examiner in the above-referenced application.

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